

FIG. 2A

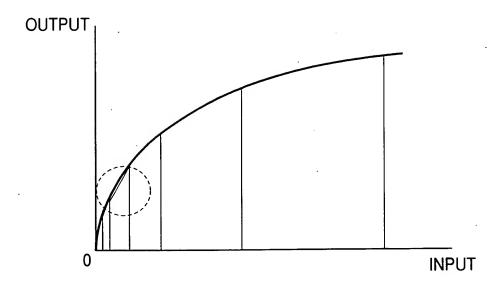


FIG. 2B

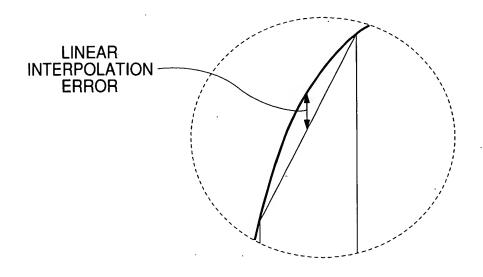


FIG. 3

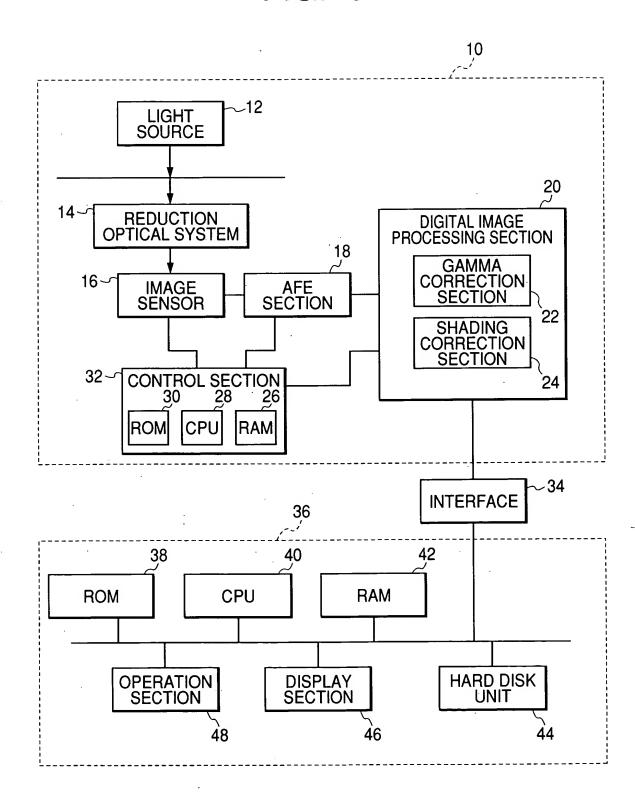


FIG. 4

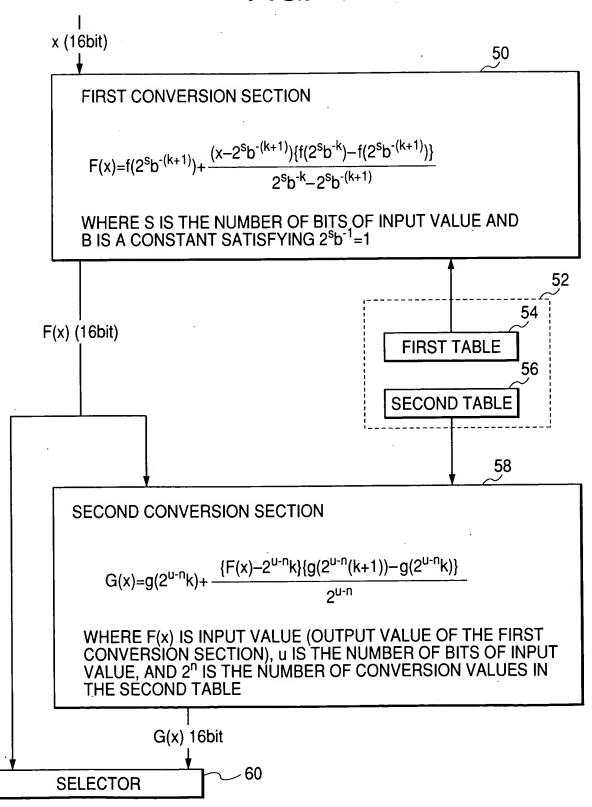


FIG. 5

		INPUT VALUE															ADDRESS
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
	1	1	1	1	1												1100_1111
LOW-ORDER	1	1	1	1	0												1100_1110
11-BIT					_							_					
INTERPOLATION	1	0	0.	0	1												1100_0001
	1	0	0	0	0								_			_	1100_0000
LOW-ORDER	0	1	1	1	1	1						\dashv					1011_1111
10-BIT INTERPOLATION	0	1	0	0.	0	0											1011_0000
LOW-ORDER	0	0	1	1	1	1	1										1010_1111
9-BIT	<u> </u>												\dashv				1010 0000
INTERPOLATION \	0	0	1	0	0	0	0									_	1010_0000
LOW-ORDER 8-BIT	0	0	0	1	1	1	1	1							_		1001_1111
INTERPOLATION	0	0	0	1	0	0	0	0					-				1001_0000
	0	0	0	0	1	1	1	1	1					-			1000_1111
LOW-ORDER 7-BIT	0	U	U	0			<u> </u>										1000_1111
INTERPOLATION .	0	0	0	0	1	0	0	0	0								1000_0000
LOW-ORDER	Ō	0	0	0	0	1	1	1	1	1							0111_1111
6-BIT																	
INTERPOLATION \	0	0	0	0	0	1	0	0	0	0							0111_0000
LOW-ORDER	0	0	0	0	0	0	1_	1	1_	1	1						0110_1111
5-BIT INTERPOLATION •		^	^			_	4	_		_	0						0110_0000
	0	0	0	0	0	0	0	0	0	0	1	1					0101_1111
LOW-ORDER † 4-BIT	0	0	0	0	-	0	<u> </u>	_	-!								0101_1111
INTERPOLATION	0	0	0	0	0	0	0	1	0	0	0	0				-	0101_0000
LOW-ORDER	10	0	ŏ	ŏ	ŏ	Ö	ō	Ö	1	1	1	1	1				0100_1111
3-BIT	٦	Ť		Ť	_	Ť		Ť		<u> </u>							
INTERPOLATION \	0	0	0	0	0	0	0	0	1	0	0	0	0				0100_0000
LOW-ORDER	0	0	0	0	0	0	0	0	0	1	1	1	1	1			0011_1111
2-BIT	<u>_</u>	_	<u> </u>		_	_	<u> </u>	.			_					<u> </u>	0011 0000
INTERPOLATION	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1		0011_0000 0010_1111
LOW-ORDER 1-BIT	0	0	0	0	0	0	0	0	0	0	-	- -	_	-	<u>'</u> -	-	0010_1111
INTERPOLATION	0	·	0	0	0	0	0	0	0	0	1	0	0	0	0		0010_0000
	10	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0010_0000
NO	۲	۲	۲	٠-	U	٢	۲	۲	"	۲	۲	†		 ' -	 	-	
INTERPOLATION	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0000_0001
	0	Ö	Ö	Ö	Ö	Ö	Ö	Ö	Ö	0	Ö	Ō	Ö	Ō	0	0	0000_0000
	ئے	• •		<u> </u>													

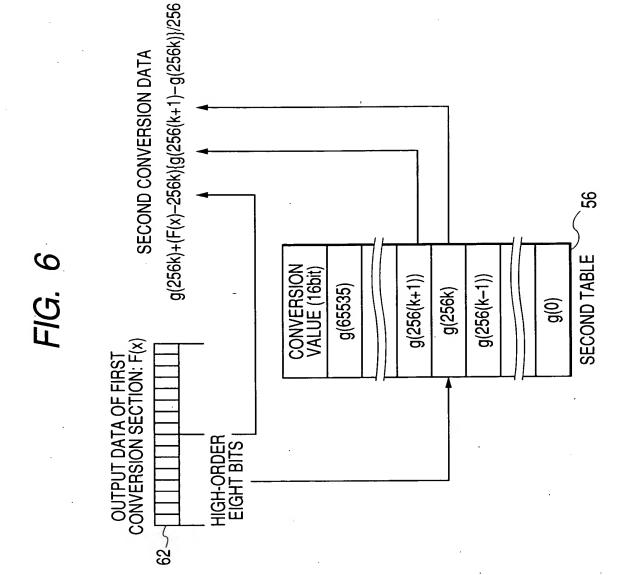
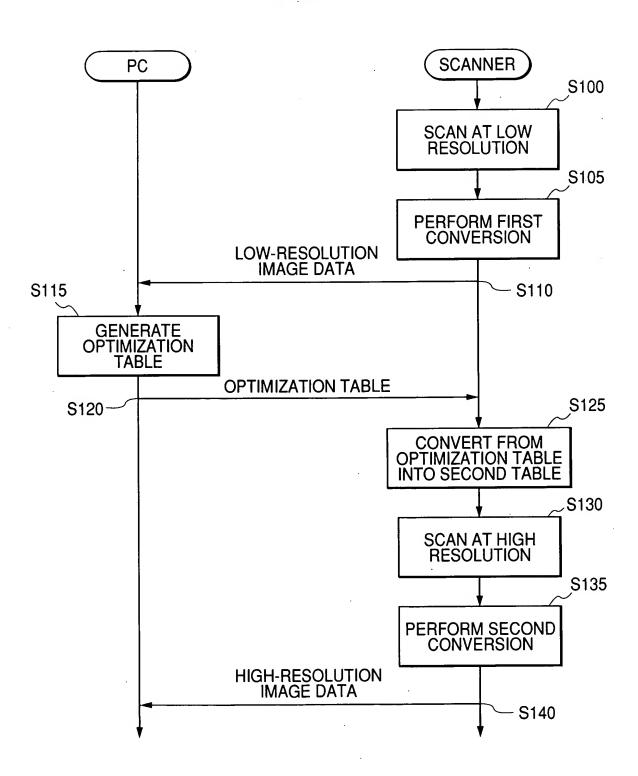


FIG. 7



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FIG. 8

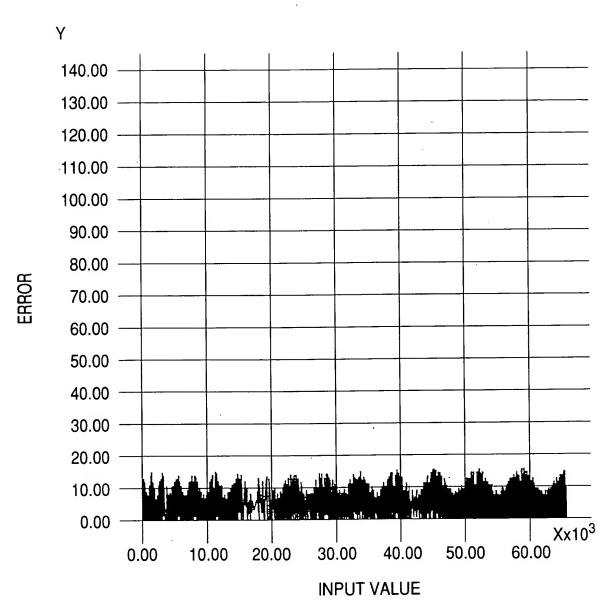


FIG. 9

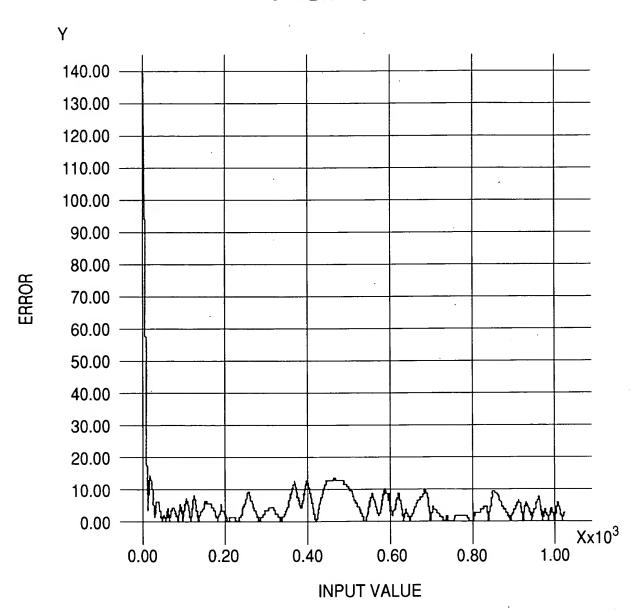


FIG. 10

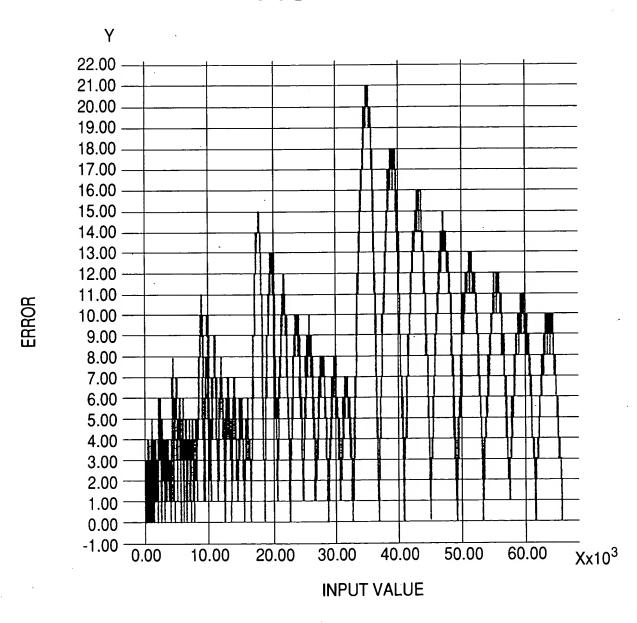


FIG. 11

